

FIG. 1

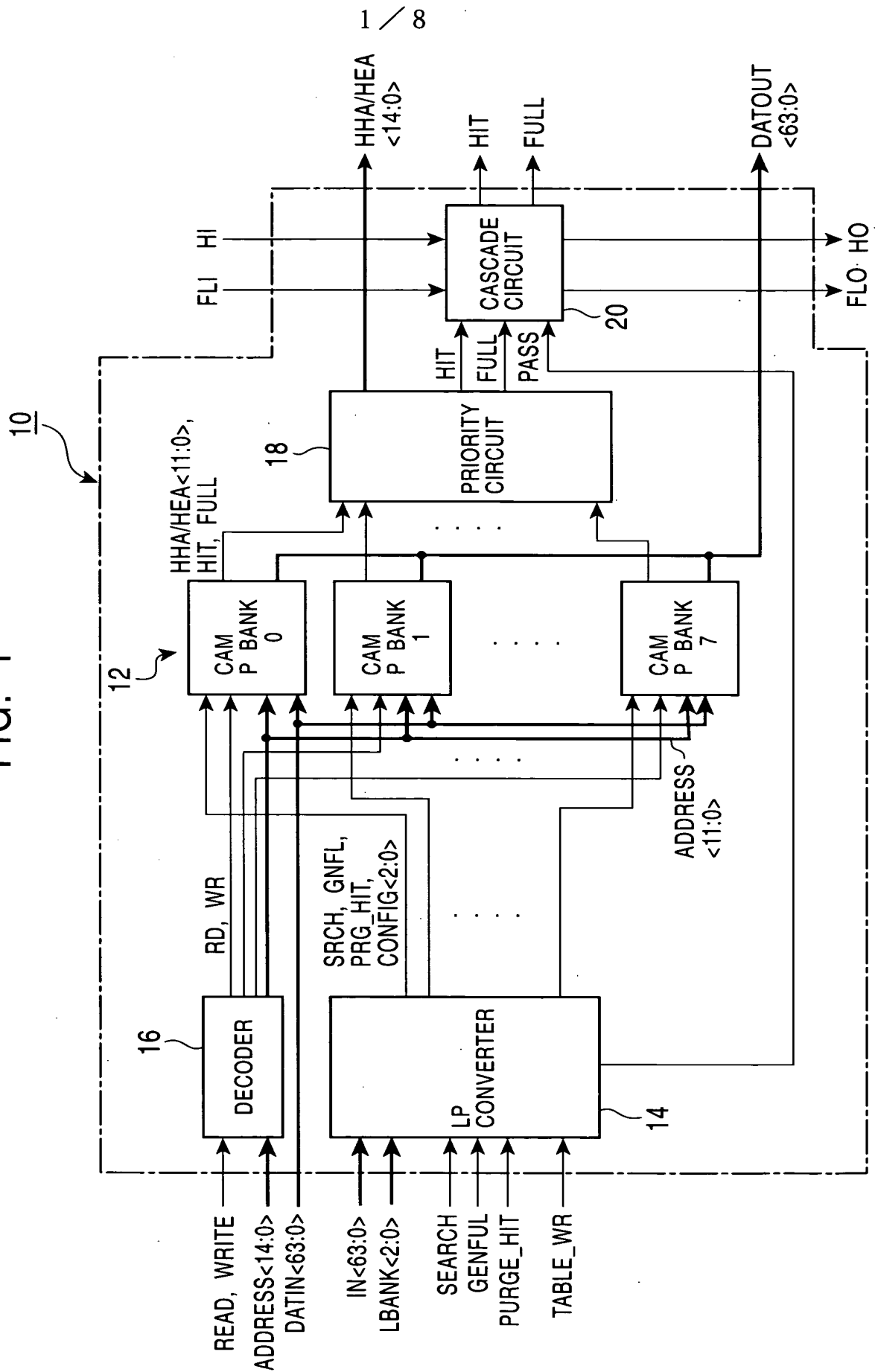


FIG. 2

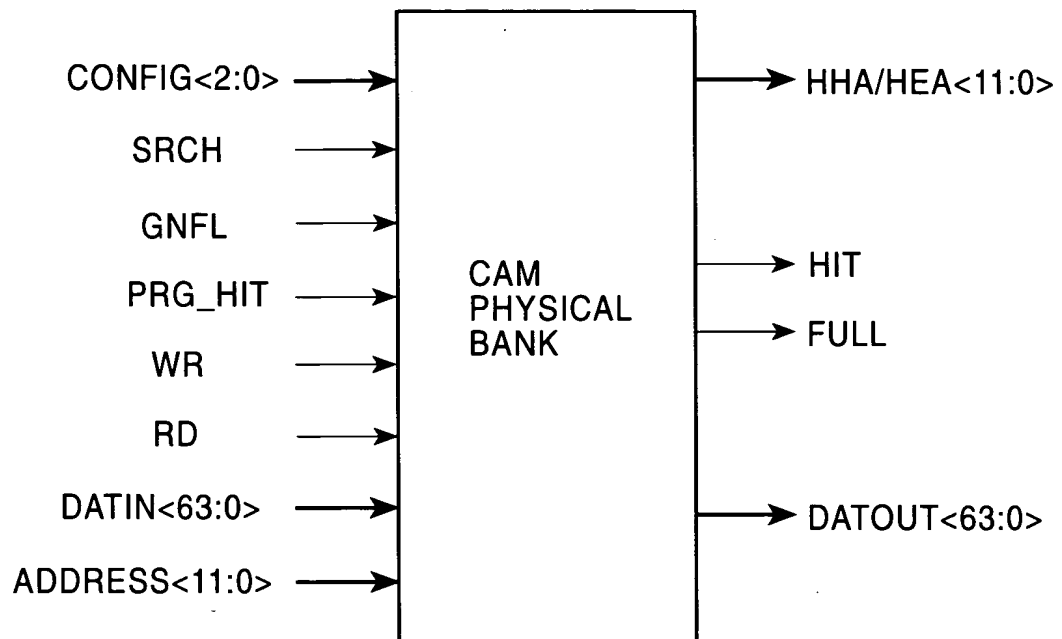


FIG. 3A

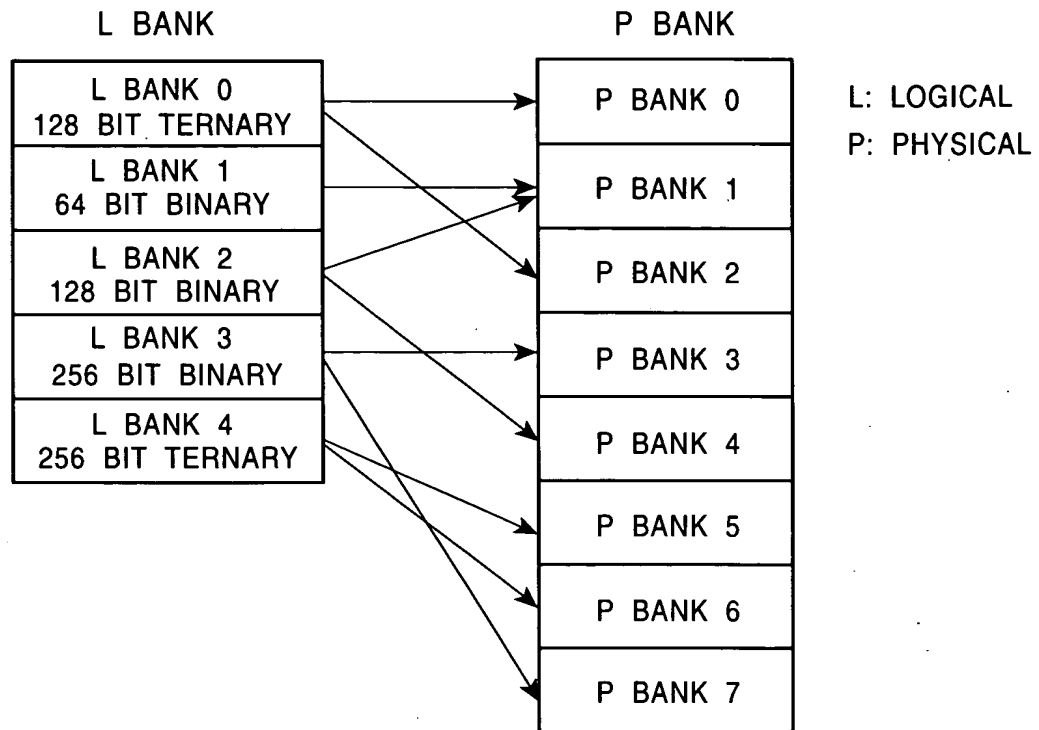


FIG. 3B

| L BANK          | CONFIG<2:0> |
|-----------------|-------------|
| 64 BIT BINARY   | 000         |
| 128 BIT BINARY  | 001         |
| 256 BIT BINARY  | 010         |
| 64 BIT TERNARY  | 100         |
| 128 BIT TERNARY | 101         |
| 256 BIT TERNARY | 110         |

FIG. 4

|          | P BANK<br>0 | P BANK<br>1 | P BANK<br>2 | P BANK<br>3 | P BANK<br>4 | P BANK<br>5 | P BANK<br>6 | P BANK<br>7 | TABLE<br>CONFIGU-<br>RATION | TABLE<br>OUTPUT | CONFIG<br><2:0> |
|----------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-----------------------------|-----------------|-----------------|
| L BANK 0 | 1           | 0           | 1           | 0           | 0           | 0           | 0           | 0           | 128 BIT<br>TERNARY          | 10100000        | 101             |
| L BANK 1 | 0           | 1           | 0           | 0           | 0           | 0           | 0           | 0           | 64 BIT<br>BINARY            | 01000000        | 000             |
| L BANK 2 | 0           | 1           | 0           | 0           | 1           | 0           | 0           | 0           | 128 BIT<br>BINARY           | 01001000        | 001             |
| L BANK 3 | 0           | 0           | 0           | 1           | 0           | 0           | 0           | 1           | 256 BIT<br>BINARY           | 00010001        | 010             |
| L BANK 4 | 0           | 0           | 0           | 0           | 0           | 1           | 1           | 0           | 256 BIT<br>TERNARY          | 00000110        | 110             |
| L BANK 5 | 0           | 0           | 0           | 0           | 0           | 0           | 0           | 0           | NOT USED                    | 00000000        |                 |
| L BANK 6 | 0           | 0           | 0           | 0           | 0           | 0           | 0           | 0           | NOT USED                    | 00000000        |                 |
| L BANK 7 | 0           | 0           | 0           | 0           | 0           | 0           | 0           | 0           | NOT USED                    | 00000000        |                 |

ASSIGN = "1"  
NO ASSIGN = "0"

FIG. 5

|          | P BANK<br>0 | P BANK<br>1 | P BANK<br>2 | P BANK<br>3 | P BANK<br>4 | P BANK<br>5 | P BANK<br>6 | P BANK<br>7 |
|----------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| L BANK 0 | BIT <0>     | BIT <1>     | BIT <2>     | BIT <3>     | BIT <4>     | BIT <5>     | BIT <6>     | BIT <7>     |
| L BANK 1 | BIT <8>     | BIT <9>     | BIT <10>    | BIT <11>    | BIT <12>    | BIT <13>    | BIT <14>    | BIT <15>    |
| L BANK 2 | BIT <16>    | BIT <17>    | BIT <18>    | BIT <19>    | BIT <20>    | BIT <21>    | BIT <22>    | BIT <23>    |
| L BANK 3 | BIT <24>    | BIT <25>    | BIT <26>    | BIT <27>    | BIT <28>    | BIT <29>    | BIT <30>    | BIT <31>    |
| L BANK 4 | BIT <32>    | BIT <33>    | BIT <34>    | BIT <35>    | BIT <36>    | BIT <37>    | BIT <38>    | BIT <39>    |
| L BANK 5 | BIT <40>    | BIT <41>    | BIT <42>    | BIT <43>    | BIT <44>    | BIT <45>    | BIT <46>    | BIT <47>    |
| L BANK 6 | BIT <48>    | BIT <49>    | BIT <50>    | BIT <51>    | BIT <52>    | BIT <53>    | BIT <54>    | BIT <55>    |
| L BANK 7 | BIT <56>    | BIT <57>    | BIT <58>    | BIT <59>    | BIT <60>    | BIT <61>    | BIT <62>    | BIT <63>    |

FIG. 6

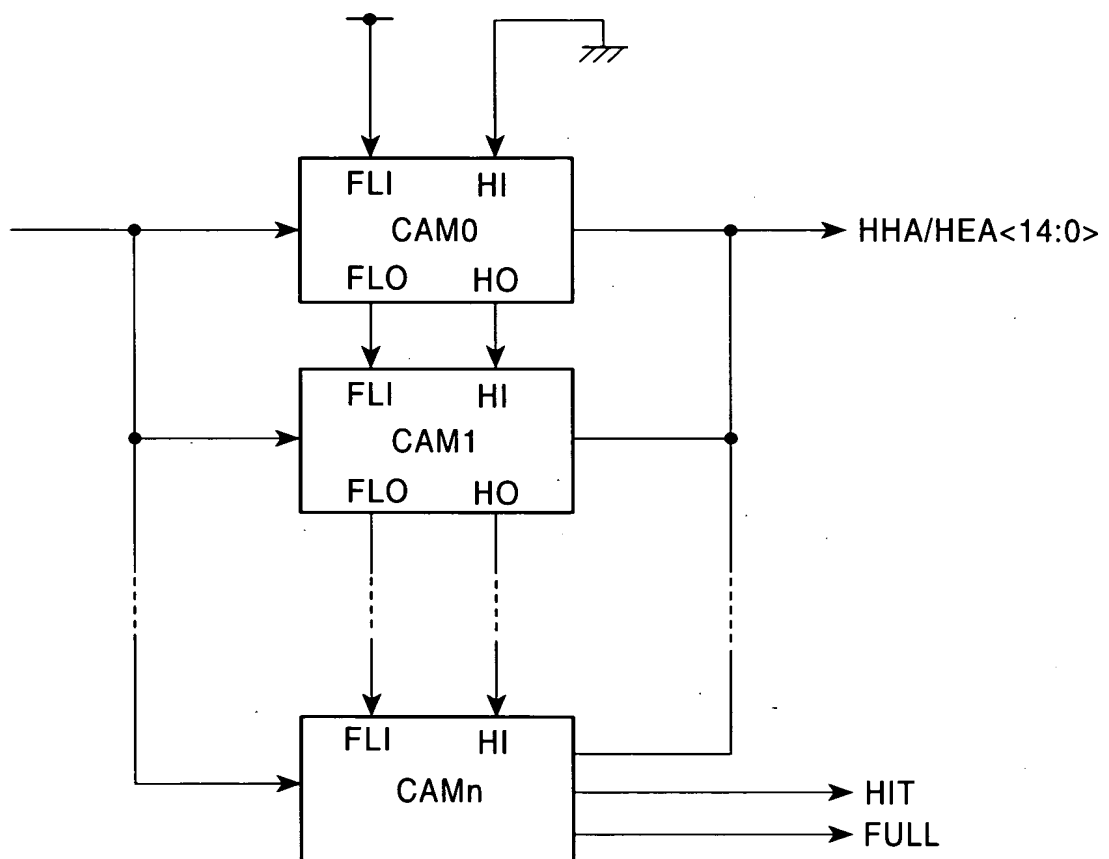


FIG. 7

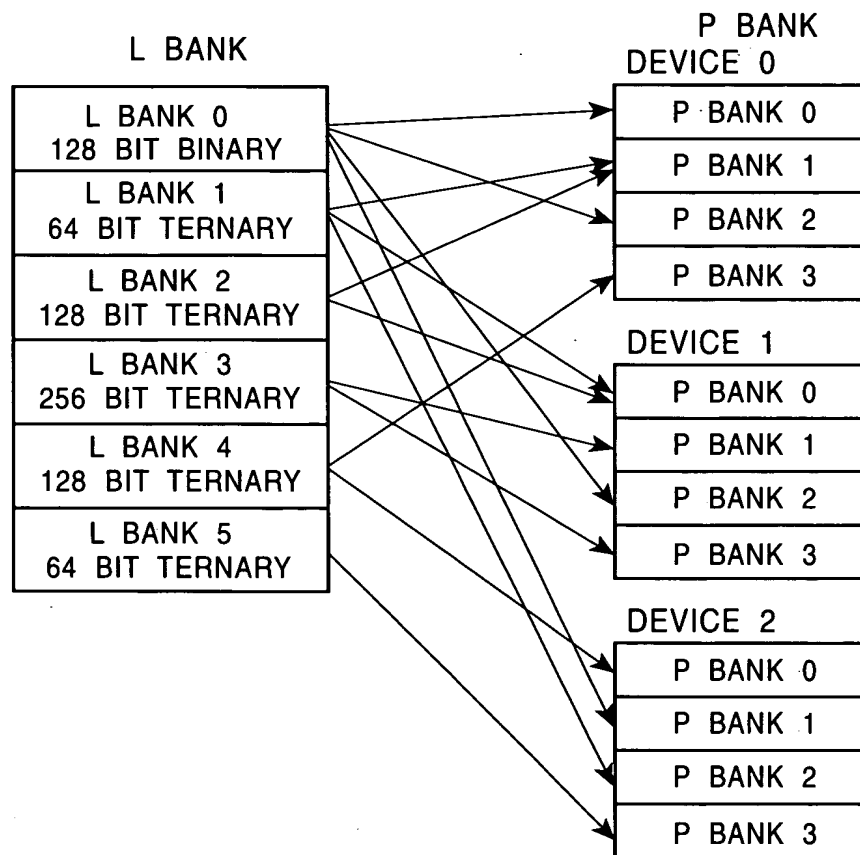


FIG. 8  
PRIOR ART

